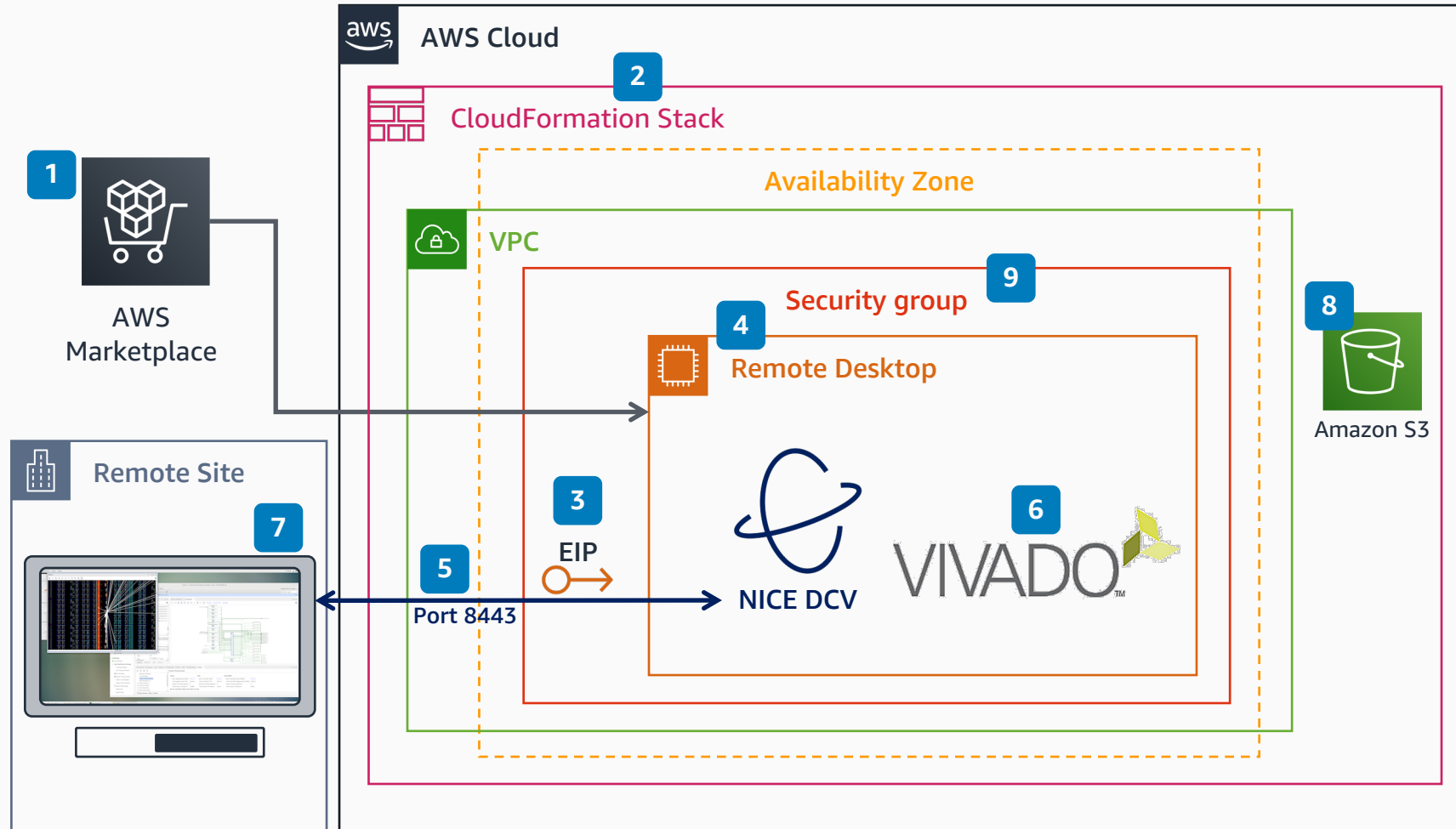


Remote Desktop for EDA

Launch Xilinx Vivado Design Suite using NICE DCV Remote Desktop on AWS

Workshop location is on AWS Samples: <https://github.com/aws-samples/aws-remote-desktop-for-eda>



- 1** Subscribe to the **FPGA Developer AMI**, located in **AWS Marketplace**. The Xilinx Vivado Design Suite is included with this AMI.
- 2** Specify required parameters (VPC, Subnet - AZ, etc.) and launch the **AWS CloudFormation** stack.
- 3** (Optional). Create an Elastic IP address (persistent IP).
- 4** Choose a remote desktop instance type that works for your tools.
- 5** Connect to **NICE DCV** using the **NICE DCV client** or over a web browser, using port 8443.
- 6** In the **FPGA Developer AMI**, launch the Xilinx Vivado Design Suite, and by typing "vivado" in a terminal window.
- 7** The remote desktop is displayed on the engineer's local system
- 8** (Optional). Configure **Amazon S3** bucket access to load design data.
- 9** (Optional). Specify additional existing security groups

